

What is claimed is:

- Sub A1
1. A power on/off circuit apparatus, comprising:
 - a power on/off circuit for controlling an on/off of power supply to electronic equipments from an external power source;
 - a microcomputer for controlling said power on/off circuit based on an operation input of a power switch;
 - a reset circuit for giving a reset signal to a reset terminal of the microcomputer when a power is supplied to said microcomputer; and
 - a non-volatile memory for storing a power on/off information just before said power switch is operated,
 - said power switch being connected to said reset terminal.
 2. The power on/off circuit apparatus according to claim 1, wherein when said power switch is operated, said microcomputer reads a power on/off information of said non-volatile memory so as to determine a power on/off state just before the power switch is operated, and writes a power-on information to said non-volatile memory while making a power-on operation if the microcomputer is in a power-off state, and further, writes a power-off information to said non-volatile memory while making a power-off operation if the microcomputer is in a power-on state.
 3. The power on/off circuit apparatus according to claim 1, wherein further includes a power circuit connected to an AC power source,
 - said power on/off circuit uses an output of said power circuit as a power source, and
 - said microcomputer uses an output of said power circuit as a power source, and
 - senses a key scan of a key matrix on which various input keys except said power switch are

arranged, regardless of the on/off of power supply to electronic equipments by said power on/off circuit.

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4. The power on/off circuit apparatus according to claim 1, wherein when said power switch is operated, a reset terminal of said microcomputer is connected to a GND, and said microcomputer is reset so as to cancel a hang-up of the microcomputer.

5. The power on/off circuit apparatus according to claim 3, wherein when the electronic equipments are operating in a power saving mode, said power on/off circuit cancels a power supply to the electronic equipments, and said microcomputer senses a key scan of the key matrix other than said power switch.

6. An electronic equipment including the power on/off circuit apparatus describe in claim 3.

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